

# ENGR2200 : Digital Systems

Analysis and design of basic combinatorial and sequential logic, Boolean algebra, Karnaugh maps, counters, adders, shift registers, multiplexers, and latches. ADC and DAC data conversion, field programmable gate arrays (FPGAs) and application-specific integrated circuits (ASICs). **Fee:** Additional fee required.

**Credits** 3

**Concurrent**

ENGR2200L

**Corequisites**

[ENGR2200L](#)

[ENGR1100](#) or [COMP1220](#).

**Fees**

\$330